





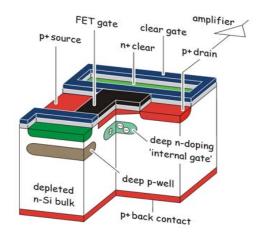
The DEPFET-Project: European Collaboration for a Pixel SVD @ SuperBelle (PXD)

JUSTUS-LIEBIG-UNIVERSITAT GIESSEN

halbleiterlabo



C. Kiesling, Max-Planck-Institute for Physics, Munich



- Report from the German SFF meeting
- Work Packages Update
- Schedule
- Conclusions & Outlook

Workshop on Prospects of future Super Flavor Factories

Motivation: German groups should make a decision

→ whether to engage in activities in a Super Flavor Factory, and

→ if yes, in which one: at KEK "SuperKEKB" or Frascati ("SuperB")

- Grants from the German government need to be requested for the coming funding period by Dec. 5, 2008
- Date: 31. Oct. 2008 and 1. Nov. 2008 (Fri/Sat)

Place: MPI Munich

60 participants from 13 German universities (& outside)

Bochum, Bonn, Dortmund, Göttingen, Gießen, Heidelberg, Karlsruhe, Mainz, Munich (MPI, LMU, TUM), Regensburg, Siegen

Friday 31 October 2008

Introduction - Auditorium (11:00-12:00)

time [id] title	presenter
11:00 [25] Welcome address	CALDWELL, Allen
11:05 [24] Aim of the Workshop (I)	LENSKE, Horst
11:15 [2] Status and Prospects for B-Physics	UWER, Ulrich

The Physics Case for a Super Flavor Factory (I) - Auditorium (13:00-15:30)

time [id] title	presenter
13:00 [4] The CKM Parameters	LACKER, Heiko
13:30 [6] Hadronic Two-Body Decays	FLEISCHER, Robert
14:00 [5] CP Violation and Hadronic B-Decays	BUCHALLA, Gerhard
14:30 [7] Physics at the Y(5S) and ISR Perspectives	DENIG, Achim
15:00 [8] Charm and Bottom Spectroscopy	PETERS, Klaus

The Physics Case for a Super Flavor Factory (II) - Auditorium (16:00-19:00)

time [id] title	presenter
16:00 [1] Aim of the Workshop (II)	MäTTIG, Peter
16:10 [9] FCNC Processes and Rare Decays	NIERSTE, Ulrich
16:40 [10] Lepton Flavor Violation	PAES, Heinrich
17:10 [11] Weak Interactions of Charm	BIGI, Ikarus
17:40 [12] Flavor Theory Perspective	BURAS, Andrzej

Saturday 01 November 2008

The Super Flavor Factory Projects (I) - Auditorium (08:30-10:00)

time	[id] title	presenter
08:30	[13] The SuperKEKB Project	YAMAUCHI, Masa
09:15	[14] Machine Aspects of the SuperKEKB	OIDE, Katsunobu

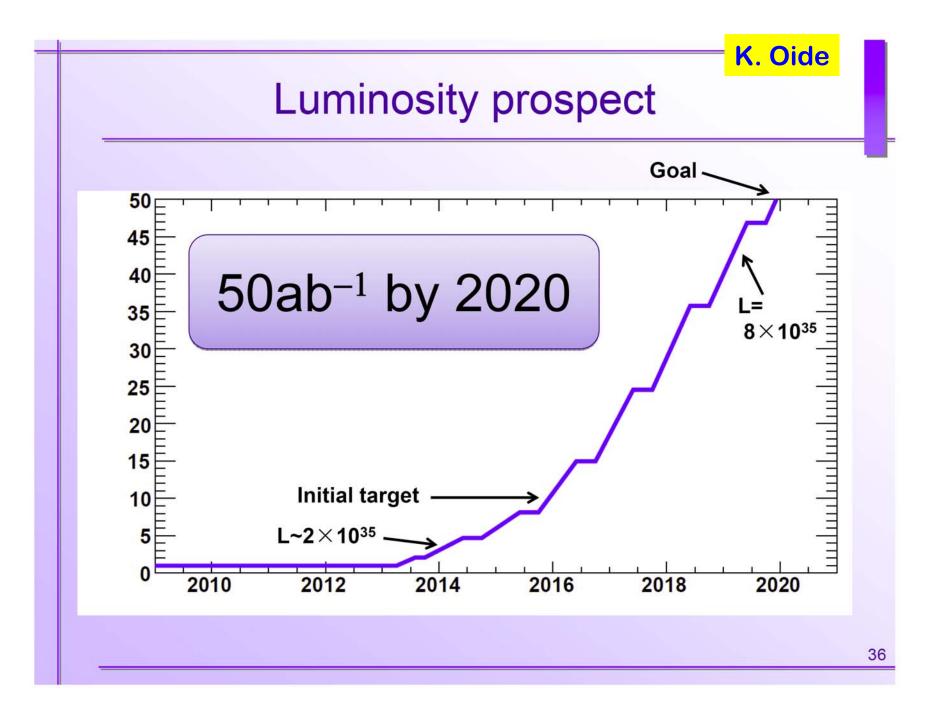
The Super Flavor Factory Projects (II) - Auditorium (10:30-12:30)

time [id] title	presenter
10:30 [15] The SuperB Project	HITLIN, David
11:15 [16] Machine Aspects of SuperB	HITLIN, David
12:00 [17] RECFA Recommendations	NAKADA, Tatsuya

German Interests in a Super Flavor Factory - Auditorium (13:30-15:00)

time [id] title	presenter
13:30 [21] Report of the MPI Group	MOSER, Hans-Günther
13:40 [20] Report of the Karlsruhe Group	FEINDT, Michael
13:50 [22] Report of the Bonn Group	WERMES, Norbert
14:00 [23] Report of the Giessen Group	LANGE, Soeren
14:10 [26] Report of the Göttingen Group	FREY, Ariane

<u>Concluding Discussion</u> - Auditorium (15:00-16:00)



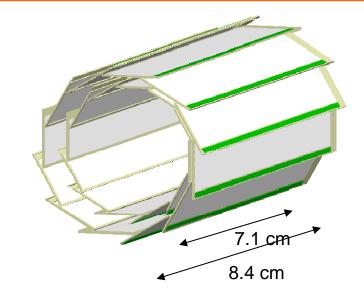
Concluding Discussions of the Munich Meeting

- Theoretical colleagues unanimously supporting SFF
- DEPFET will be baseline for SuperBelle (M. Yamauchi)
 DEPFET can also be installed in SuperB detector (D. Hitlin)
- 6 German groups expressed interest to participate in the SuperKEKB/SuperBelle project
- No group expressed interest to participate in SuperB
- Some German groups are busy with LHCB, but expressed their opinion that a Super-B-Factory (> 50 /ab) opens a new Era of Flavor Physics and must be pushed



Very positive outcome of the German Meeting for SuperKEKB

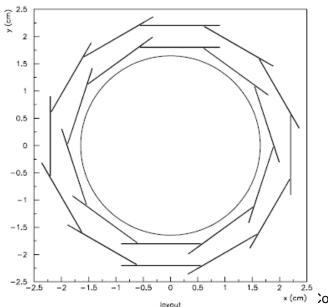
DEPFET Pixel Detector



Small, thin (50µm) Detector: 20 – 24 Modules (one sensor each)

Beam pipe radius (presently): 1.5cm initially with upgrade to 1.0 cm later

Radii still subject to optimisation:

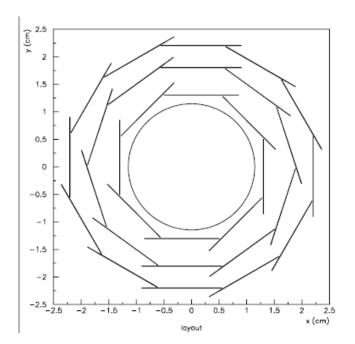


Likely scenario:

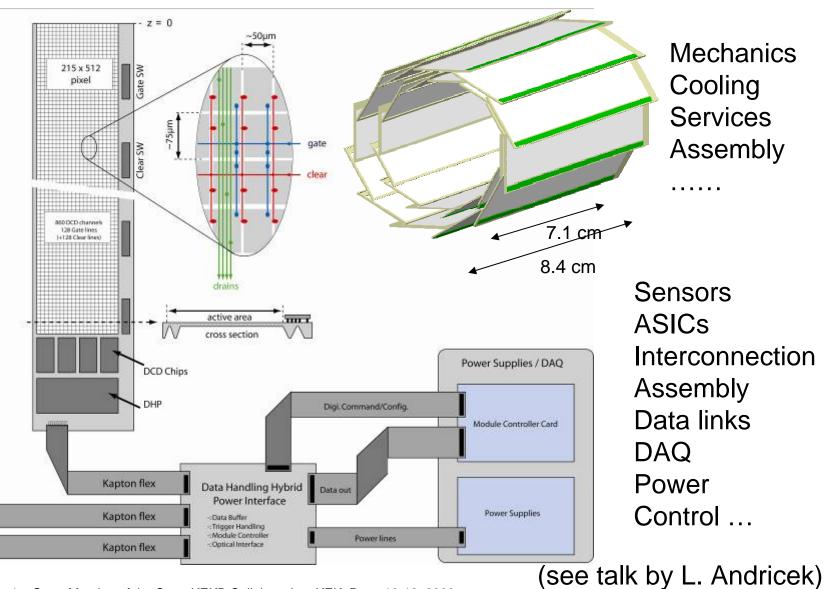
Layer 1 at 1.8 cm Layer 2 at 2.2 cm

Upgrade option: add Layer 0 at 1.3 cm

x (cm) Collaboration, KEK, Dec.. 10-12, 2008



DEPFET Pixel Sensors, details



Main R&D Issues

Sensors:	pixel geometry -> parameter studies prototyping, radiation hardness (> 10Mrad), thinning (see Laci's talk)
Read-out ASICs:	Current Digitizer chip (DCD): prototype OK, needs test at full speed (x2) (< O(1%) occupancy) Switcher: rad-hard design, speed OK, redesign for SuperBelle
DHP & Data Link:	Zero-suppr: 400 Gpx/s -> 2 Gpx/s (trigger, occ) -> 1.6 Gb/s per half module
DAQ:	80 Gb/s total -> Gießen R&D for Panda (32 Gb/s)
Test Procedures:	Beam/system tests (see Peter's talk)
Mechanics, Cooling: C. Kiesling, 1st Open Meeting of the SuperKEKI	Mounting, vibrations, thermal issues, alignment, B Collaboration, KEK, Dec 10-12, 2008 9

List of Institutions contributing to DEPFET @ SuperBelle

C			Contact
Germany	MPI BON GIE GOE HEI KAR	Max-Planck-Institute for Physics, Munich University of Bonn University of Giessen University of Göttingen University of Heidelberg University of Karlsruhe	C. Kiesling, HG. Moser N. Wermes S. Lange A. Frey P. Fischer T. Müller
Austria	VIE	Institute for High Energy Physics (HEPHY), Vienna	M. Friedl
Czech Rep.	PRA	Charles-University Prague	P. Kodys
Poland	KRA	Institute of Nuclear Physics, Krakow	H. Palka
Spain	IFV URL UBA CNM IFB USC IFC	Instituto de Fisica Corpuscular (IFIC), Valencia University Ramon Llull, Barcelona University of Barcelona Centro Nacional de Microelectronica, Barcelona Instituto de Fisica d'Altes Energies (IFAE), Barcelona University of Santiago de Compostela Instituto de Fiisica de Cantabria (IFCA), Santander	C. Lacasta J. Riera Babures L. Garrido E. Cabruja M. Chmeissani P. Vazquez Regueiro I. Vila
USA	HAW	University of Hawaii	G. Varner
Japan	KEK	KEK	T. Tsuboyama

Work Packages and Assignments

Nr.	Work Package	Lead Institution	Collab. Institutions
1.0	DEPFET Modules		
1.1	Parameter Definitions	MPI	KRA, PRA
1.2	Sensor Development	MPI	
1.3	ASIC Development		
1.3.1 1.3.2	Switcher DCD	HEI	
1.3.3 1.3.4	Data Handling Processor (DHP) Data link	BON	MPI, UBA USC, URL
1.4	Module Design		
1.4.1	Sensor Ladder	MPI	HEI, BON, IFV, CNM, IFB
1.4.2	Kapton Flex	KEK	VIE, BON
1.4.3	Data Handling Hybrid (DHH)	KEK	VIE, BON

Work Packages and Assignments (cont.)

Nr.	Work Package	Lead Institution	Collab. Institutions
1.5	Mechanical Design	MPI	KAR, VIE; KRA, IFV, IFB
1.6	Thermal Issues	KAR	MPI, VIE, KRA, IFV, IFB
1.7	System		
1.7.1	Data Acqusition board	GOE	KRA, GIE, MPI, KEK, URL, HAW
1.7.2	Power supplies with slow control	KRA	KEK, USC
1.7.3	Cooling plant (refigerator, heat exchanger)	KEK	

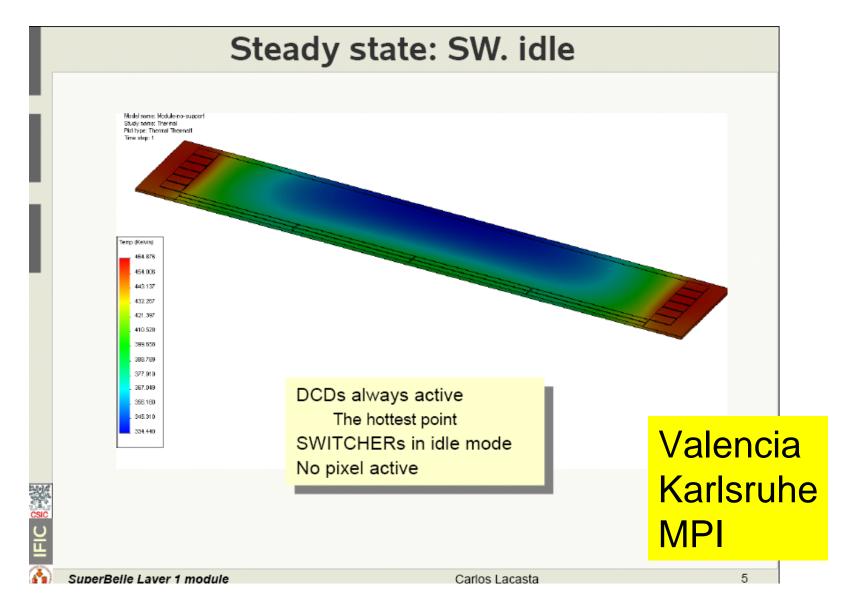
Work Packages and Assignments (cont.)

Nr.	Work Package	Lead Institution	Collab. Institutions
2.0	Test Facilities		
2.1	Test beams	PRA	KAR, BON, VIE, IFV, IFC URL,CNM, IFB, USC
2.2 2.3	Setups for thermal tests Mechanical mockup	KAR	MPI, VIE, IFV, USC, IFC
3.0	Integration and running-in scenario		
4.0	Operation Issues		

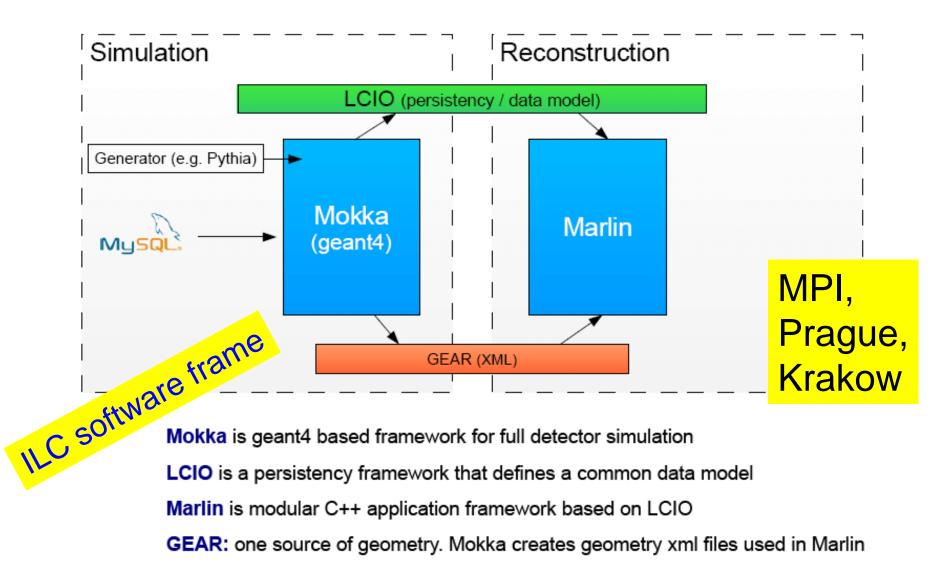
DEPFET-Collaboration:

total of 18 Institutes from 7 Countries

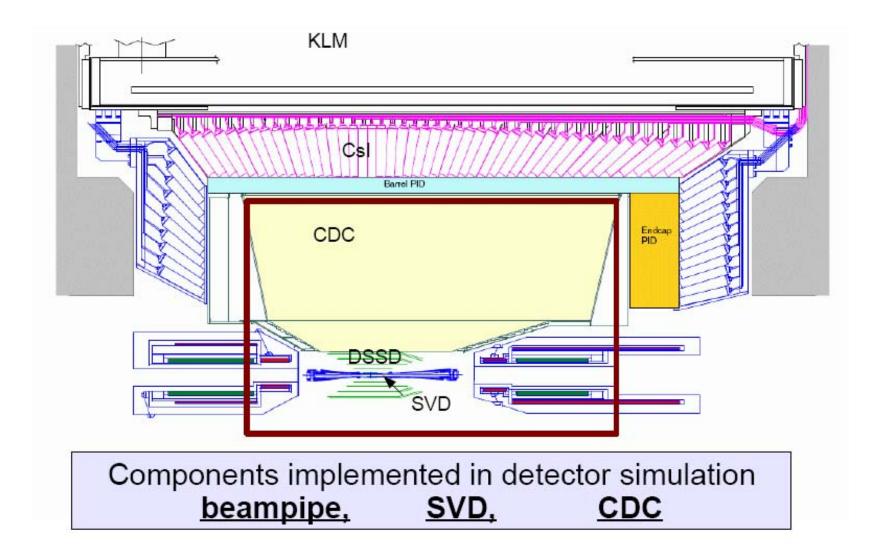
Example of Work Package "Thermal Issues"

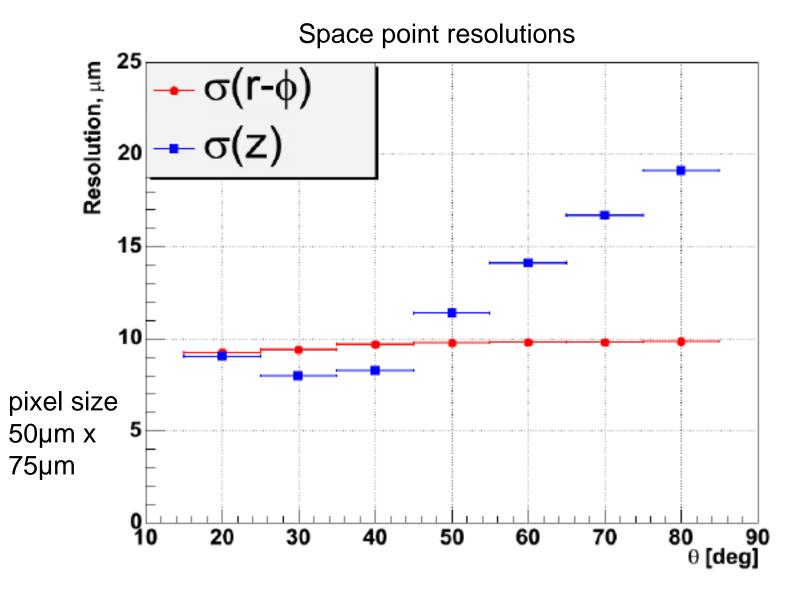


Example of Work Package "Parameter Definitions"



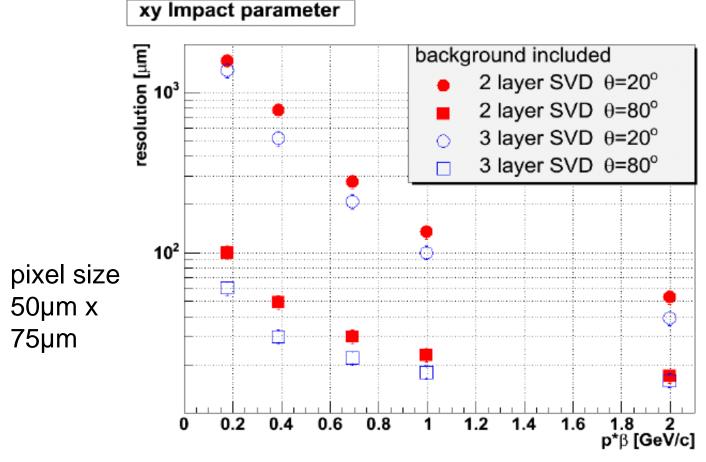
Example of Work Package "Parameter Definitions" (cont.)





C. Kiesling, 1st Open Meeting of the SuperKEKB Collaboration, KEK, Dec.. 10-12, 2008

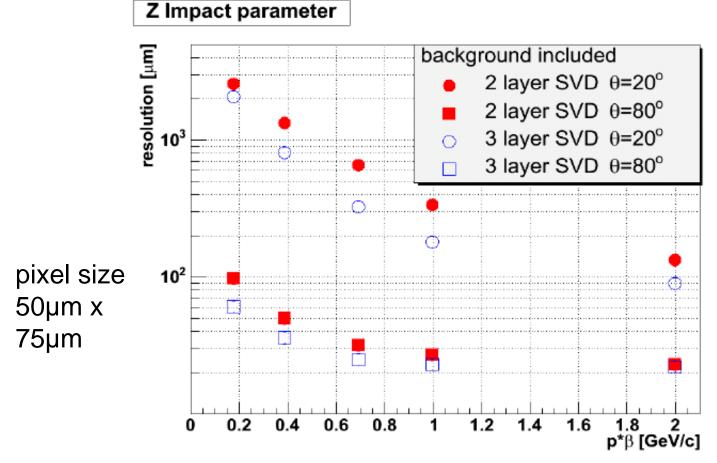
Impact Parameter Resolution in Presence of Backgrounds



C. Kiesling, 1st Open Meeting of the SuperKEKB Collaboration, KEK, Dec.. 10-12, 2008

Example of Work Package "Parameter Definitions" (cont.)

Impact Parameter Resolution in Presence of Backgrounds



C. Kiesling, 1st Open Meeting of the SuperKEKB Collaboration, KEK, Dec.. 10-12, 2008

Example of Work Package "Power & Slow Control"

Low Voltage Power Supplies (LVPS)

Planning Short term	 agree on design parameters find/select a designer 	Krakow USC (Spain)
Long term	 workout a detailed list of com elaborate a technical specification estimate costs prototype procurement select the manufacterer 	

informal contact established with Fideltronik, the producer of HVPS for ATLAS

http://www.fideltronik.com.pl

Installation & Operation

- Module assembly (where, how ?)
- Assembly of modules to a layer (or complete detector)
- Installation in SuperBelle
- Operation issues (calibration, monitoring)
 - Online monitoring and logging: Currents Temperatures Occupancy Signal and pedestal distributions (pedestal updating!) Noise Radiation dose

Schedule

finish checks on radiation hardness up to 10Mrad 2009: demonstrate fast readout with existing components fix geometry & technology prepare DEPFET production (SOI wafers)

prototype production (processing: ~ 1.5 years) ¹/₂ SuperBelle module size (one electrical unit) some parameter variations

- test and evaluation of prototypes 2010: assembly procedure and tooling Very tight schedule, not without risk
- final detector production 2011:
- assembly + tests 2012:
- 2013: installation

Schedule (detail)

					2008	2009	2010	2011	2012	2013
ID	Task Name	Duration	Start	Finish	0tr 2 0tr 3 0tr 4 0	2009 htt 110tr 210tr 310tr 4	2010	2011 4 Otr 1 Otr 2 Otr 3 Otr	2012 4 Qtr 1 Qtr 2 Qtr 3 Qtr 4	2013 Otr 1 Otr 2 Otr 3 Otr 4
1	1.0 DEPFET Detector	1278 days?	Wed 09/07/08	Fri 31/05/13						
2	1.1 Parameter Definitions	560 days?	Wed 09/07/08	Tue 31/08/10						
4	1.2 Pixel Sensor	1035 days?	Wed 09/07/08	Tue 26/06/12					•	
5	R&D on thin oxides	196 days?	Pri 07/11/08	Fri 07/08/09			 			
12	PXD6 Design	188 days?	Wed 09/07/08	Fri 27/03/09		_ _				
13	Layout Deadline (Chip	0 days	Tue 23/12/08	Tue 23/12/08	•	23/12				
14	Wafer Delivery	0 days	Mon 13/10/08	Mon 13/10/08	♦ _13/1	0				
15	SOI preparation	55 days	Mon 05/01/09	Fri 20/03/09	5					
20	PXD6 production	301 days?	Mon 30/03/09	Mon 24/05/10		+	₽ ₽			
24	thinning	45 days	Tue 25/05/10	Mon 26/07/10			*			
30	PXD6 evaluation	266 days?	7 Tue 25/05/10	Tue 31/05/11						
31	fix final module dimens	0 days	Fri 31/07/09	Fri 31/07/09		<mark>♦ 31/07</mark>				
32	PXD7 Design	70 days?	7 Tue 23/11/10	Tue 01/03/11			1			
33	wafer delivery	0 days	Wed 01/09/10	Wed 01/09/10			01/0	9		
34	SOI preparation	55 days	Wed 01/09/10	Tue 16/11/10			, , , , , , , , , , , , , , , , , , ,			
39	PXD7 production	301 days?	7 Tue 01/03/11	Tue 24/04/12				₩	∲ייי	
43	thinning	45 days	Wed 25/04/12	Tue 26/06/12					, ⊕¥⊕	
49	1.3 ASIC	1058 days?	Wed 09/07/08	Fri 27/07/12						
50	1.3.1 DCD	998 days?	Wed 09/07/08	Fri 04/05/12						
60	1.3.2 Switcher	1058 days?	Wed 09/07/08	Fri 27/07/12						
72	1.3.3 DHP	849 days?	Wed 29/10/08	Mon 30/01/12					i -	
92	1.3.4 Data Link	380 days?	7 Thu 10/07/08	Wed 23/12/09			-			

Schedule (detail)

					2008	2009	2010	2011	2012	2013
94	1.4 Module	1175 days?	? Wed 09/07/08	Tue 08/01/13						7
95	1.4.1 Sensor ladder	1175 days?	Wed 09/07/08	Tue 08/01/13						2
105	Module Production	130 days	s Wed 27/06/12	Tue 25/12/12	2					ı
132	1.4.2 Kapton link	859 days?	Wed 09/07/08	Mon 24/10/11						
137	1.4.3.DHH	839 days?	Wed 09/07/08	Mon 26/09/11				₽		
141	1.5 Mechanical Design	1169 days?	Wed 09/07/08	Mon 31/12/12	2					,
145	1.6 Thermal Issues	1170 days?	Wed 09/07/08	Tue 01/01/13	· •					,
149	1.7 System	1278 days?	Wed 09/07/08	Fri 31/05/13						
158	2.0 Test Systems	849 days?	? Tue 01/07/08	Fri 30/09/11			1 1 1			
167	Intergration/Installation	180 days	s Wed 19/09/12	Tue 28/05/13	3					
168	Installation of 1st layer	3 mons	s Wed 19/09/12	Tue 11/12/12	2				_	
169	Installation of 2nd layer	3 mons	3 Wed 26/12/12	Tue 19/03/13	3					_ 1
170	service connection	2 wks	s Wed 20/03/13	Tue 02/04/13	3					5
171	tests & debug	2 mons	s Wed 03/04/13	Tue 28/05/13	3					
172	System ready at KEK	0 days	s Tue 28/05/13	Tue 28/05/13	3					a 28/05

Conclusions and Outlook

- DEPFET Collaboration / German groups are now firmly behind the PXD project for SuperBelle,
- Funding agencies have been approached (total of ~2.5 MEuro core cost)
- Work packages are defined, lead institutions (+contacts) are identified
- Working Groups are getting organized, work for SuperBelle intensifying (regular meetings)
- Groups working towards DEPFET PXD baseline option for SuperBelle
- Schedule for the project is established and agreed (but tight!)
- SuperBelle Note for DEPFET PXD is being planned